

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"6198820".pn.	USPAT	OR	OFF	2005/07/06 13:16
S2	5	("6198820").URPN.	USPAT	OR	OFF	2005/06/30 11:43
S3	4	("5758132"   "5842141"   "5872960"   "5909585").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/30 11:46
S4	5554	(drive adj signal) near3 (device apparatus peripheral circuit)	USPAT	OR	OFF	2005/06/30 12:44
S5	13	(drive adj signal) near3 (external adj (device apparatus peripheral circuit))	USPAT	OR	OFF	2005/06/30 12:40
S6	8	(drive adj signal) near3 (external adj (device apparatus peripheral circuit))	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 12:40
S7	1	(drive adj signal) near3 (device apparatus peripheral circuit) same (device near3 prepar\$5)	USPAT	OR	OFF	2005/06/30 12:46
S8	0	(drive adj signal) near3 (device apparatus peripheral circuit) same (apparatus near3 prepar\$5)	USPAT	OR	OFF	2005/06/30 12:46
S9	0	(drive adj signal) near3 (device apparatus peripheral circuit) same (peripheral near3 prepar\$5)	USPAT	OR	OFF	2005/06/30 12:46
S10	8	(drive adj signal) near3 (device apparatus peripheral circuit) same (circuit near3 prepar\$5)	USPAT	OR	OFF	2005/06/30 13:01
S11	10	(drive adj signal) near3 (device apparatus peripheral circuit) same (circuit near3 prepar\$5)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 12:48
S12	2042	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (cpu processor microprocessor)	USPAT	OR	OFF	2005/06/30 13:11
S13	1	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (before adj (cpu processor microprocessor))	USPAT	OR	OFF	2005/06/30 13:09
S14	2	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (before adj (cpu processor microprocessor))	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 13:09
S15	615	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (cpu processor microprocessor) with signal	USPAT	OR	OFF	2005/06/30 13:11

S16	10	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (cpu processor microprocessor) with signal with (before in\$1advance ahead)	USPAT	OR	OFF	2005/06/30 13:21
S17	1	(device apparatus peripheral circuit) near3 (prepar\$5 ready wake\$1up activ\$4) near3 (cpu processor microprocessor) with signal with (before in\$1advance ahead)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 13:14
S18	307	interlocking adj control	USPAT	OR	OFF	2005/06/30 13:21
S19	0	timer adj interlocking adj control	USPAT	OR	OFF	2005/06/30 13:21
S20	37	interlocking adj control with signal	USPAT	OR	OFF	2005/06/30 13:23
S21	64	interlocking adj control with signal	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 13:23
S22	2	("5737588" "5365183").pn.	USPAT	OR	OFF	2005/07/06 13:26
S23	1	fujita.in. with yoichi	USPAT	OR	OFF	2005/07/06 13:28
S24	1	shuichi.in. with nitta	USPAT	OR	OFF	2005/07/06 13:29
S25	0	shinichi.in. with senoo	USPAT	OR	OFF	2005/07/06 13:29
S26	5	yasuaki.in. with saito	USPAT	OR	OFF	2005/07/06 13:30